X-1002 US 10/082,518

PATENT Conf. No. 5974

IN THE UNITED STATES PATENT OFFICE

Applicant:

L. James Hwang et al.

Assignee:

Xilinx, Inc.

Title:

METHOD AND SYSTEM FOR INTEGRATING CORES IN FPGA-

BASED SYSTEM-ON-CHIP (SoC)

Serial No.:

10/082,518

Filed:

February 22, 2002

Examiner:

Whitmore, Stacy A. Art Unit: 2812

Docket No.:

X-1002 US

Conf. No. 5974

Mail Stop NON-FEE AMENDMENT COMMISSIONER FOR PATENTS

P.O. BOX 1450

Alexandria, VA 22313-1450

RESPONSE TO FIRST OFFICE ACTION

Dear Sir:

Responsive to the Office Action dated October 21, 2003, please amend the above-identified application as follows:

Image

2812

5974

Modified 02-03

Application / Conf. No.

PTO/SB/21 (01-03)
Approved for use through 9/30/00. OMB 0651-0031
Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

10/082,518

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TRANSMITTAL				Filing Date		Fe	February 22, 2002		
FORM (to be used for all correspondence after initial filing)				First Named Inventor		L.	L. James Hwang		
				Examiner Name		St	Stacy Whitmore		
Mail Stop: NON-FEE AMENDMENT				Art Unit		28	2812		
Express Mail Receipt No.				Patent No.					
Total Number of Pages in This Submission			2	Attorney Docket Number		r X-	-1002 US		
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Fee Transm	nittal Forn	n	Assign	ment Papers		ТП	After Allowance Communication		
			(with Recordation Cover Sheet) Declaration / Oath			to			
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Express Abandonment Request			To Convert a Provisional Application				Other Enclosure(s) (please identify below):		
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